

LOW POWER CDMA RECEIVER**PRIORITY CLAIM**

This application is a continuation in part of U.S. patent Application Serial No. 09/539,165, entitled "Low Power CDMA Receiver" filed March 29, 2000.

FIELD OF THE INVENTION

The invention relates to the design of electronic amplifiers for receivers. In particular, it relates to achieving a low power and high linearity receiver amplifiers by means of reactively switching in and out both the amplification and filtering stages.

RELATED APPLICATIONS

This application relates to U.S. Patent Application 09/538,606, filed March 29, 2000, entitled "Low Power and High Linearity Receivers with Reactively Biased Front Ends" by the same inventor and assigned to the same assignee.

BACKGROUND OF THE INVENTION

An electronic amplifier accepts as its input an electronic signal and produces as its output a stronger version of that electronic signal. For example, recording an electrocardiogram on a chart requires amplifying the weak electrical signal produced by a beating heart until the signal is strong enough to move a pen up and down as a paper chart moves past the pen.

A linear amplifier is one in which there is a linear relationship between the electronic signal it receives as input and the electronic signal it produces as output. That is, for a change of X units in its input voltage or current, it produces